

MiiNePort E1 Schematic Design Guide

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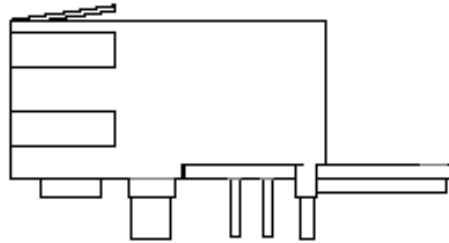
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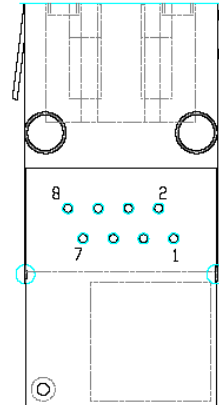
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Pin Description

(Right Side View)

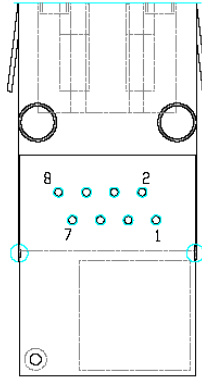


(Bottom View)



Signal Name	Pin	Funciton
GND	1	Circuit Ground.
VCC	2	+3.3V Power in.
Reset	3	External HW Reset In.
Data Out (TxD)	4	Serial Data Out.
Data IN (RxD)	5	Serial Data In.
RTS	6	Pin 6 can be configured as Ready/RTS (Request to Send),DO,or RS-485 Tx Enabled (default is Ready/RTS).
Reset to Default	7	Pin 7 can be configured as Reset to Default, DIO, Modem Control Out, or RS-485 Tx Enable, or (default is Reset to Default).
CTS	8	Pin 8 can be configured as CTS (Clear to Send), DI,Or Modem Control In (default is CTS)

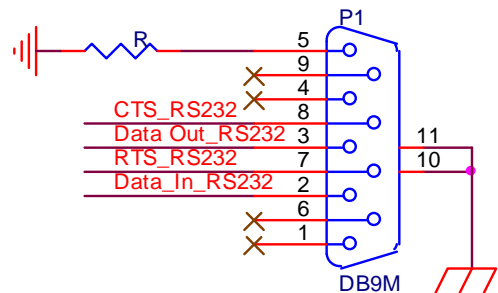
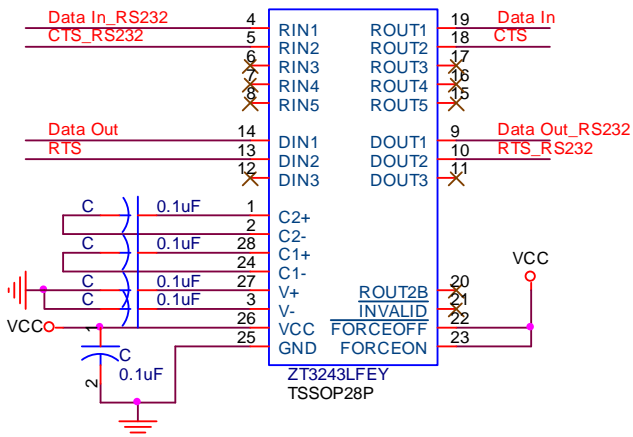
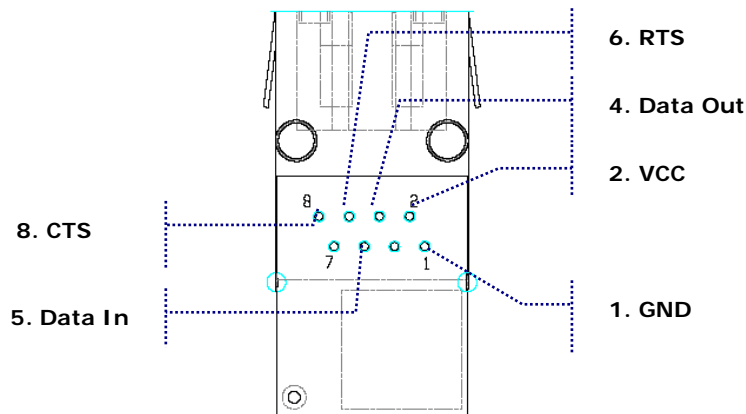
System Power Circuit Design



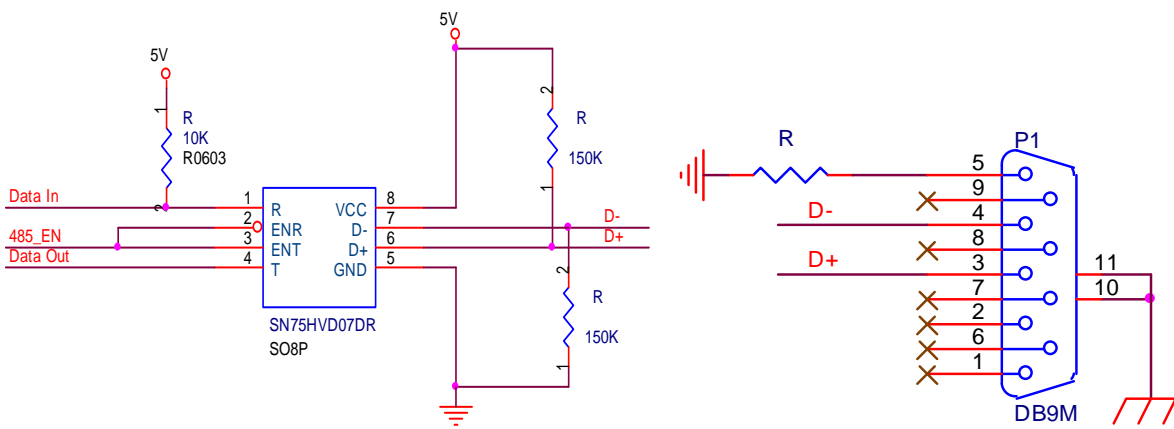
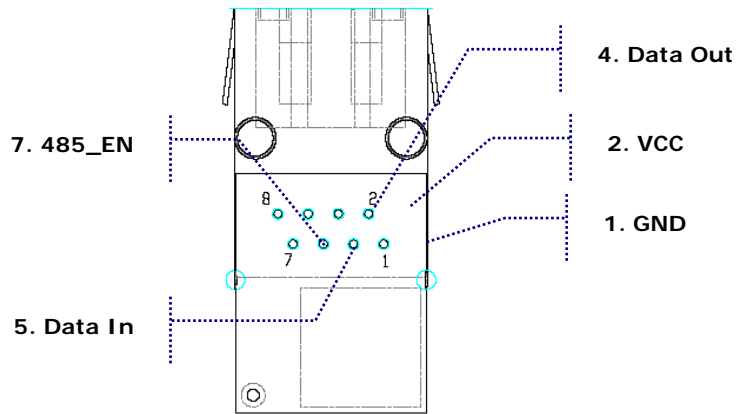
Symbol	Description	Min	Nominal	Max	Units
VCC	Supply voltage (typical 3.3V)*	3.14	3.3	3.46	V

*Power Consumption is 160mA@3.3VDC input

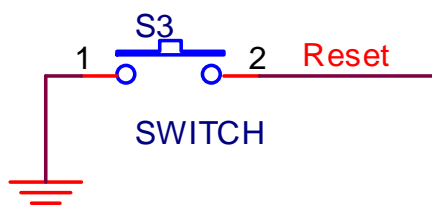
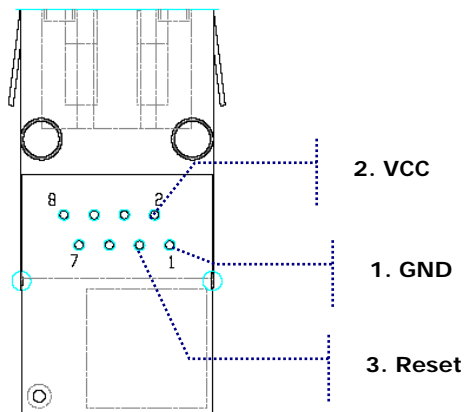
RS-232 Circuit Design



2W-RS-485 Circuit Design

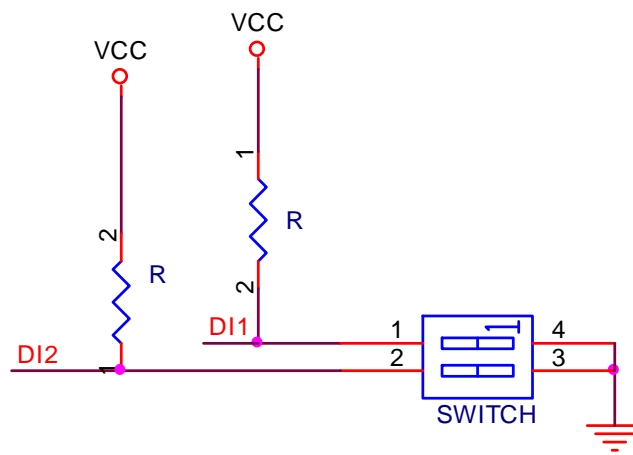
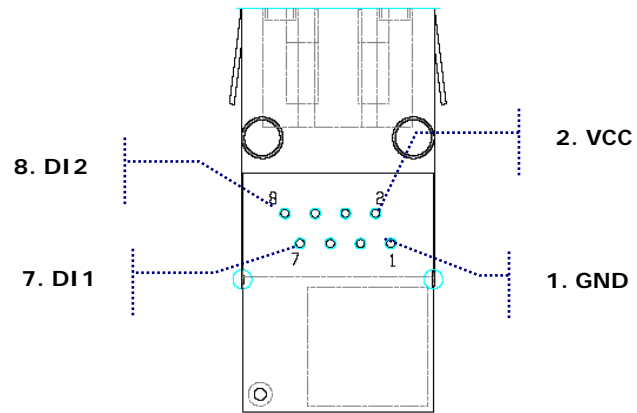


Reset Circuit Design

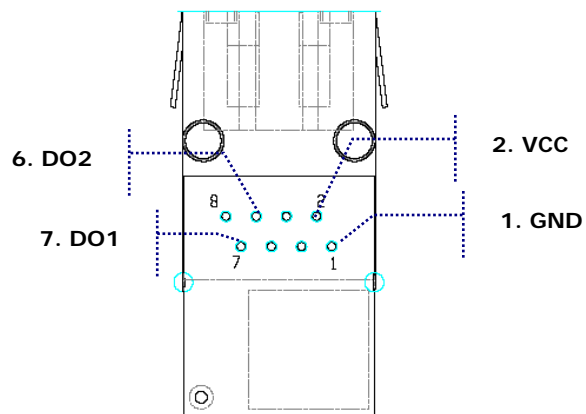


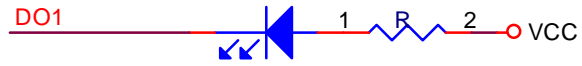
DIO Circuit Design

DI



DO





DC Characteristics for Serial PIO INTERFACE

Symbol	Parameter	Min	Typ.	Max	Units
VIL	Input Low Voltage	-0.3		0.8	V
VIH	Input High Voltage	2		5.5	V
VOL	Output Low Voltage			0.4	V
VOH	Output High Voltage		3.3		V
IOL	GPIO/Other interface	11/5.6			mA
IOH	GPIO/Other interface	12/7.2			mA
VIH	Input High Voltage	2		5.5	V